Examiner

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	INFORMATION DISCLOSURE STATEMENT BY APPLICANT			Applicati n/C nf. No.	Unknown (1) 1 (2) 8/4 (2)		
INFO				Filing Date	July 11, 2003		
STAT				First Named Invent r	Warren E. Cory		
				Art Unit	Unknown		
(us	(use as many sheets as necessary)			Examiner Name	Unknown		
Sheet	1	of	2	Attorney Docket Number	X-1065-1C US		

U.S. PATENT DOCUMENTS					
Examiner Initials	Cite No.'	Document Number Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant
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Initials No.1		Country Code ^{a -Number 4} -Kind Code ^a (if known)	MM-DD-YYYY	Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T °	
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officiation of information unless it contains a valid OMR control number. Linder the Paperwork Reduction Act of 1995, no person Complete if Known Substitute for form 1449A/PTO Applicati n / Conf. N Unknown **INFORMATION DISCLOSURE Filing Date** July 11, 2003 STATEMENT BY APPLICANT First Nam d Invent r Warren E. Cory Art Unit Unknown (use as many sheets as necessary) **Examiner Name** Unknown Sheet of Attorney Docket Number X-1065-1C US

	OTHER NON PATENT LITERATURE DOCUMENTS					
Examiner Initials *	Cite No '	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publislier, city and/or country where published.	Τ²			
pe		Cary D. Snyder and Max Baron; "Xilinx's A-to-Z System Platform"; Cahners Microprocessor; The Insider's Guide to Microprocessor Hardware; Microdesign Resources; February 6, 2001; pp. 1-5.				
ve		Xilinx, Inc.; "Virtex-II Platform FPGA Handbook"; published December 2000, available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124; pp. 33-75.				
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